

# ECS Configuration Change Request

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Page(s)

<b>1. Originator</b> Robert Cole	<b>2. Log Date:</b> 2/28/02	<b>3. CCR #:</b> 02-0174	<b>4. Rev:</b>	<b>5. Tel:</b> 301-925-0799	<b>6. Rm #:</b> 2110C	<b>7. Dept.</b> SE/HW
<b>8. CCR Title:</b> Move 2x336 MHz CPUs from e0acs05 to e0ins02						
<b>9. Originator Signature/Date</b> Robert Cole /s/ 2/28/02			<b>10. Class</b> II	<b>11. Type:</b> CCR	<b>12. Need Date:</b> 2/28/02	
<b>13. Office Manager Signature/Date</b> Jim Mather /s/ 2/28/02			<b>14. Category of Change:</b> Update ECS Baseline Doc.		<b>15. Priority:</b> (If "Emergency" fill in Block 27). Emergency	
<b>16. Documentation/Drawings Impacted:</b> 920-TDE-001			<b>17. Schedule Impact:</b>		<b>18. CI(s) Affected:</b> INTHW	
<b>19. Release Affected by this Change:</b> N/A		<b>20. Date due to Customer:</b>		<b>21. Estimated Cost:</b> None - Under 100K		
<b>22. Source Reference:</b> <input type="checkbox"/> NCR (attach) <input type="checkbox"/> Action Item <input type="checkbox"/> Tech Ref. <input type="checkbox"/> GSFC <input type="checkbox"/> Other:						
<b>23. Problem: (use additional Sheets if necessary)</b> excessive context switching on V0Gateway machine (e0ins02)						
<b>24. Proposed Solution: (use additional sheets if necessary)</b> Remove 2x168 MHz CPUs from e0ins02 and replace with 2x336 MHz CPUs from e0acs05. This will reduce the number of CPUs on the SDSRV machine from 6 to 4, however, this machine has a light CPU load and should be able to function under the reduced configuration.						
<b>25. Alternate Solution: (use additional sheets if necessary)</b>						
<b>26. Consequences if Change(s) are not approved: (use additional sheets if necessary)</b> continued excessive context switching						
<b>27. Justification for Emergency (If Block 15 is "Emergency"):</b> The work will be done 2/28/02 to take advantage of a maintenance window.						
<b>28. Site(s) Affected:</b> <input type="checkbox"/> EDF <input type="checkbox"/> PVC <input type="checkbox"/> VATC <input checked="" type="checkbox"/> EDC <input type="checkbox"/> GSFC <input type="checkbox"/> LaRC <input type="checkbox"/> NSIDC <input type="checkbox"/> SMC <input type="checkbox"/> AK <input type="checkbox"/> JPL <input type="checkbox"/> EOC <input type="checkbox"/> IDG Test Cell <input type="checkbox"/> Other						
<b>29. Board Comments:</b>				<b>30. Work Assigned To:</b>	<b>31. CCR Closed Date:</b>	
<b>32. EDF/SCDV CCB Chair (Sign/Date):</b>			<b>Disposition:</b> Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ECS			
<b>33. M&amp;O CCB Chair (Sign/Date):</b> Gary Gavigan /s/ 3/5/02			<b>Disposition:</b> <b>Approved</b> App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ECS			
<b>34. ECS CCB Chair (Sign/Date):</b>			<b>Disposition:</b> Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ESDIS			